

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A configurable circuit arrangement comprising at least one circuit component at which a load is applied that can vary during operation of said circuit arrangement, wherein said configurable circuit arrangement comprises:

~~a.~~ load determination means for determining a load applied at said at least one configurable circuit component having different fan-in or fan-out depending on a configuration of said circuit arrangement; ~~and~~

~~b.~~ adjusting means for switching off a buffer connected to the configurable circuit according to the determination of the applied load, responsive to said determination means, adjusting, wherein switching off the buffer adjusts a drive capacity of said at least one circuit component to a value less than a maximum drive capacity while still meeting a delay specification.

2. (previously presented) A circuit arrangement according to claim 1, wherein said determination means is configured to determine said load based on a configuration information loaded to said circuit arrangement.

3. (previously presented) A circuit arrangement according to claim 2, wherein said configuration information is stored in a configuration memory.

4. (previously presented) A circuit arrangement according to claim 2, wherein said configuration information comprises a configuration bit stream defining at least one of an input load and an output load of said at least one component.

5. (canceled)

6. (canceled)

7. (currently amended) A circuit arrangement according to claim 1, ~~claim 5~~, wherein said adjusting means is adapted to generate at least one control signal for simultaneously switching off a section of buffers on or off said buffer sections.

8. (currently amended) A circuit arrangement according to claim 7, ~~claim 6~~, wherein said adjusting means is adapted to derive said control signal ~~only~~ from a most significant bit signal of a selection signal obtained from said determination means.

9. (previously presented) A circuit arrangement according to claim 1, wherein said adjusting means is configured to vary a threshold voltage of circuit elements of said circuit arrangement.

10. (previously presented) A circuit arrangement according to claim 9, wherein said adjusting means is adapted to change at least one bias voltage responsive to said determination means.

11. (previously presented) A circuit arrangement according to claim 1, wherein said circuit arrangement is a field programmable gate array device.

12. (currently amended) A method of controlling power consumption of a configurable circuit arrangement, said method comprising the steps of:

~~a. determining a load applied at~~ applied to at least one circuit component having different fan-in or fan-out depending on a configuration of said configurable circuit arrangement; and

~~b. switching off a buffer connected to the configurable circuit according to the determination of the applied load, adjusting~~ wherein switching off the buffer adjusts a drive capacity of said at least one circuit component responsive to said determination step to a value less than a maximum drive capacity while still meeting a delay requirement.

13. (new) The method according to claim 12, further comprising simultaneously switching off a section of buffers.

14. (new) The method according to claim 13, further comprising deriving said control signal from a most significant bit signal of a selection signal.